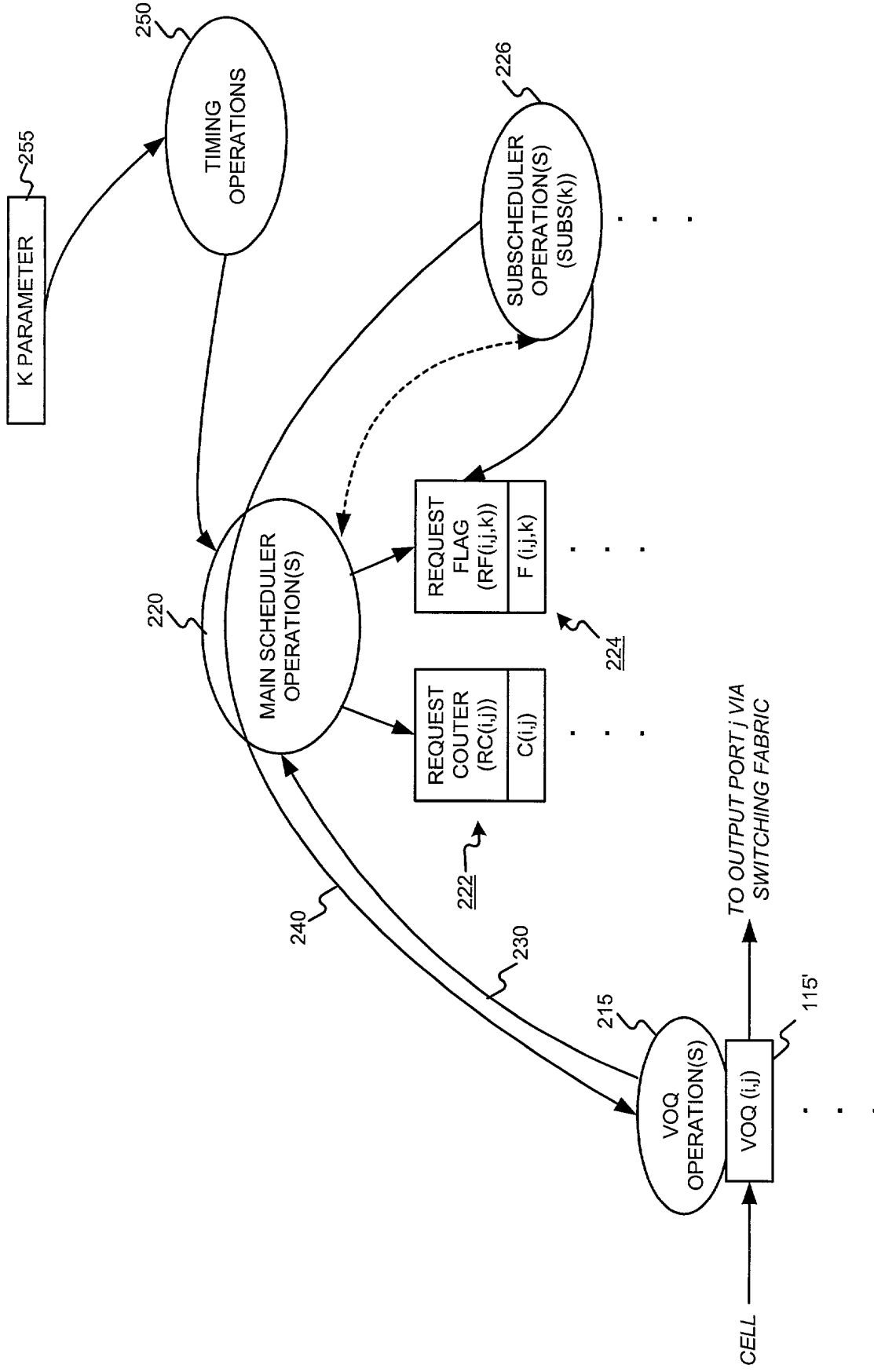


100

FIGURE 1



200
FIGURE 2

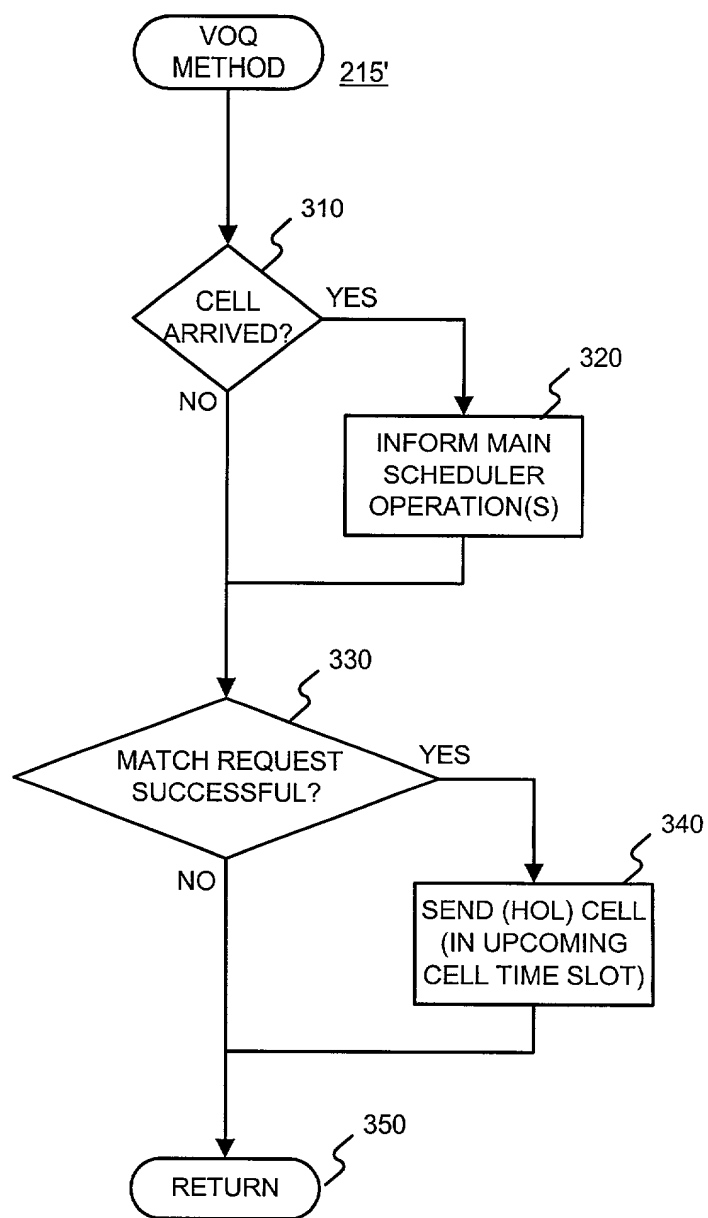


FIGURE 3

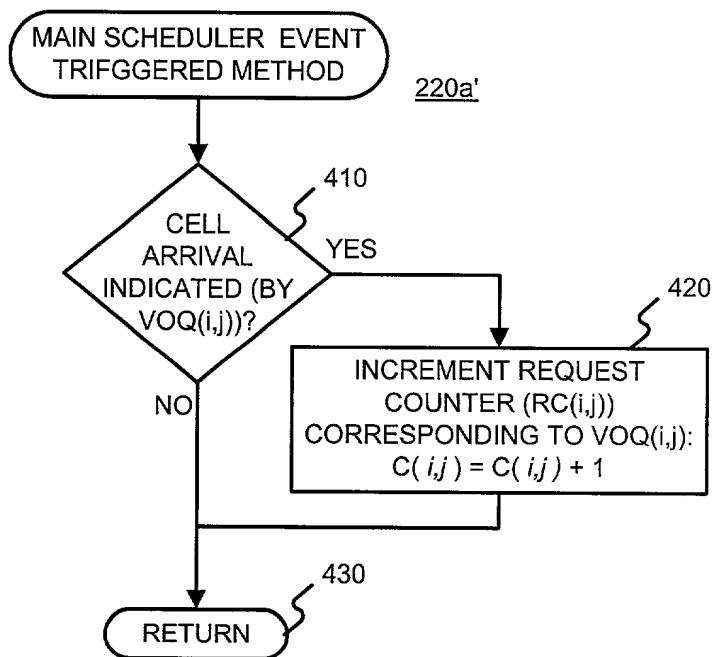


FIGURE 4A

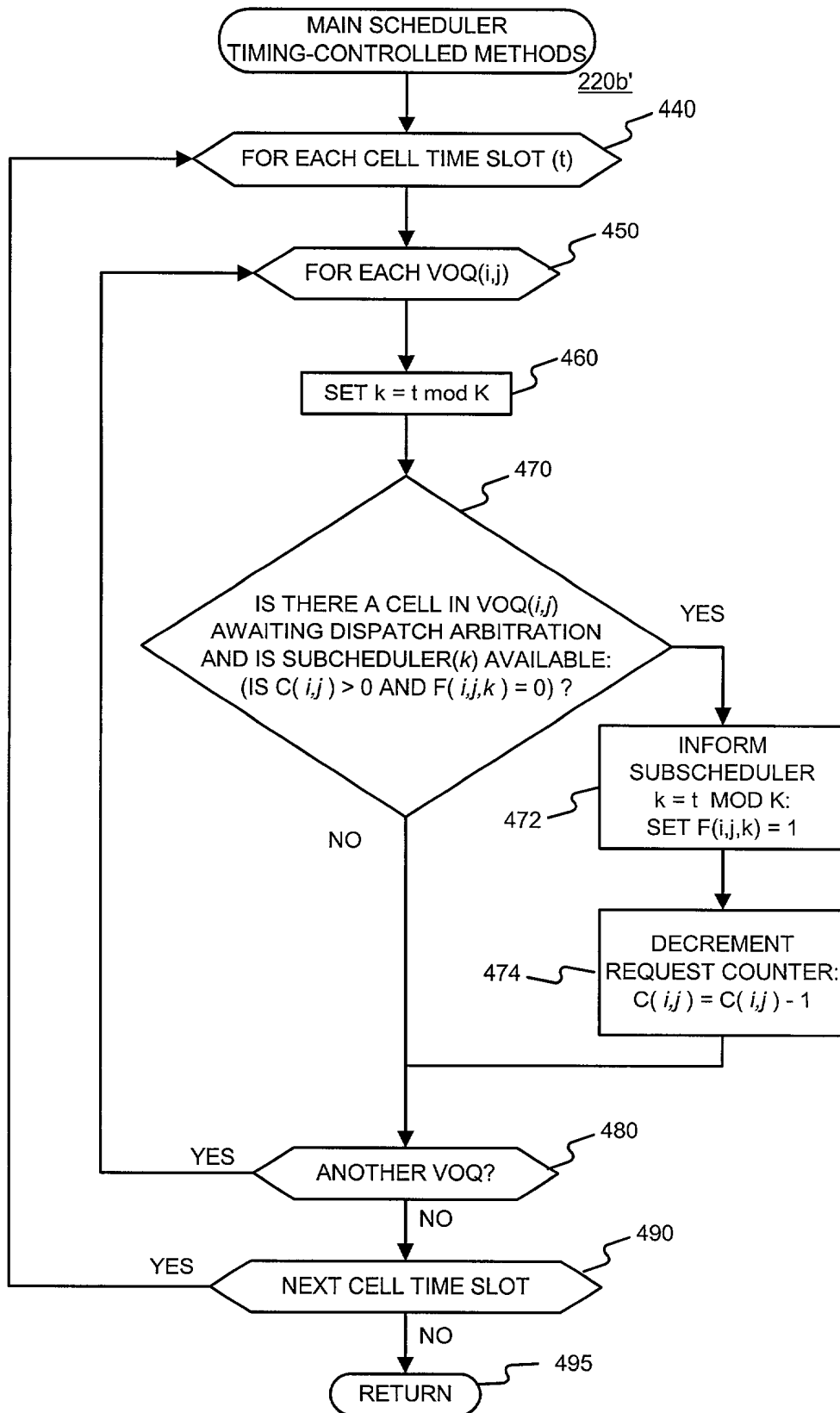


FIGURE 4B

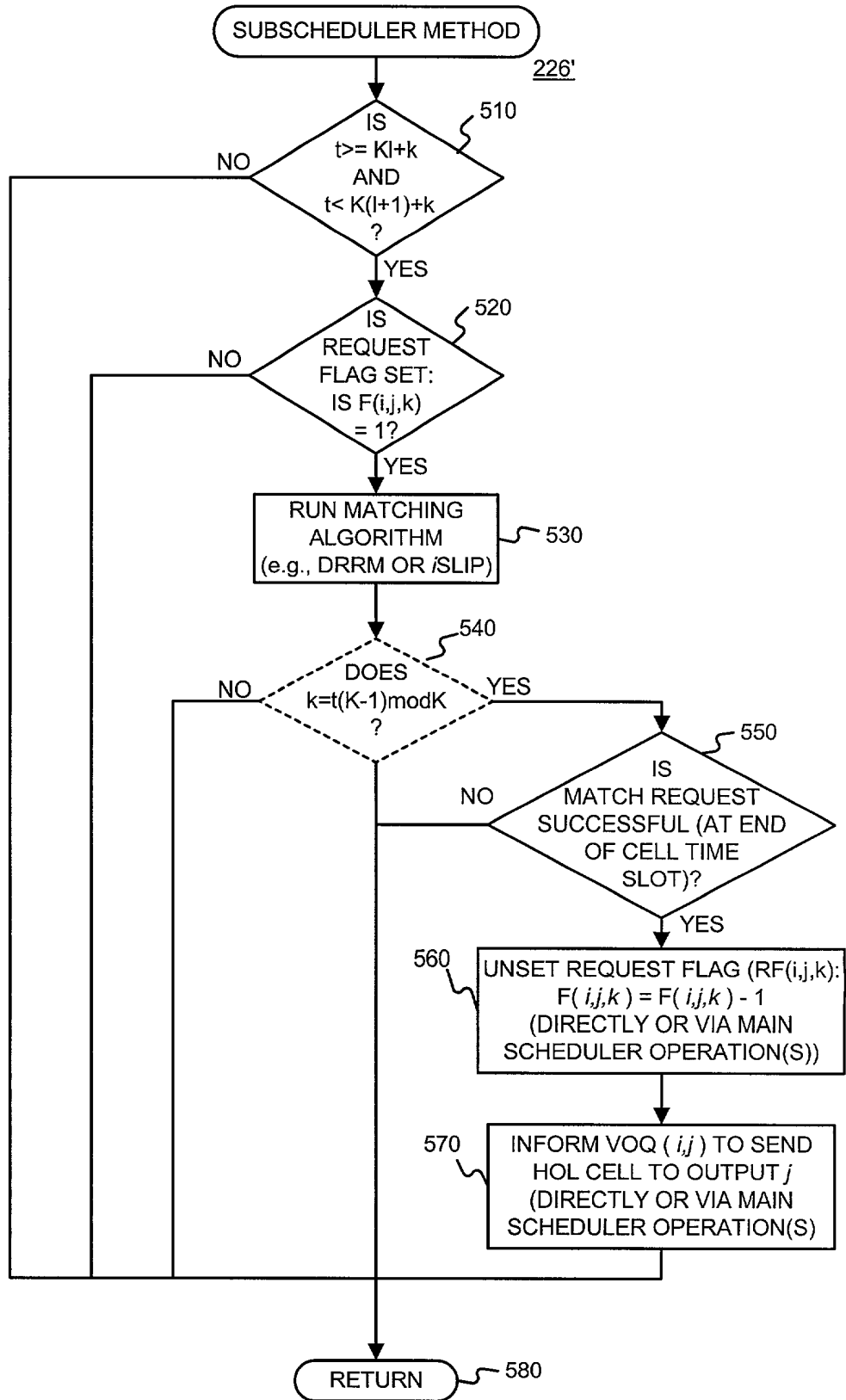


FIGURE 5

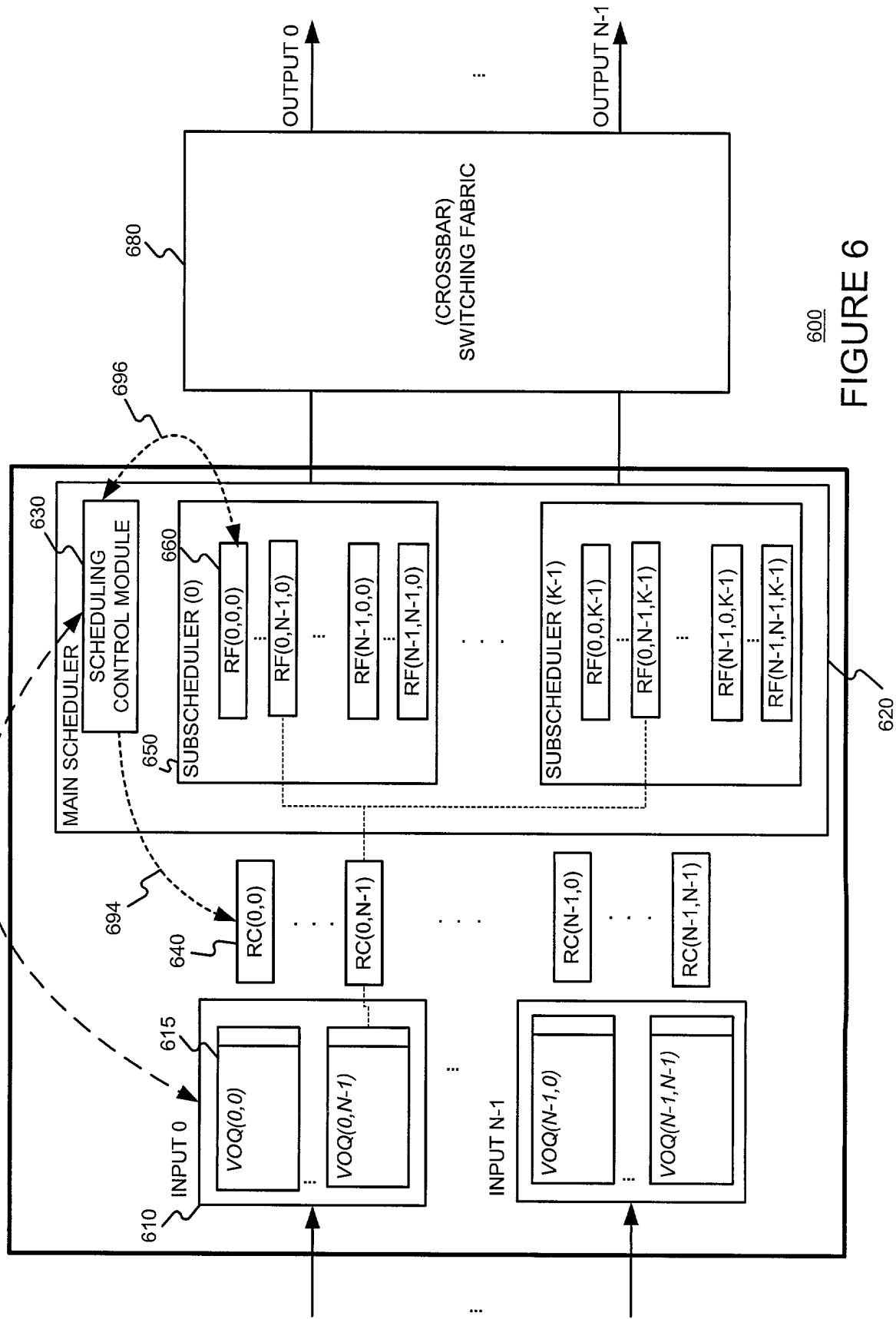


FIGURE 6

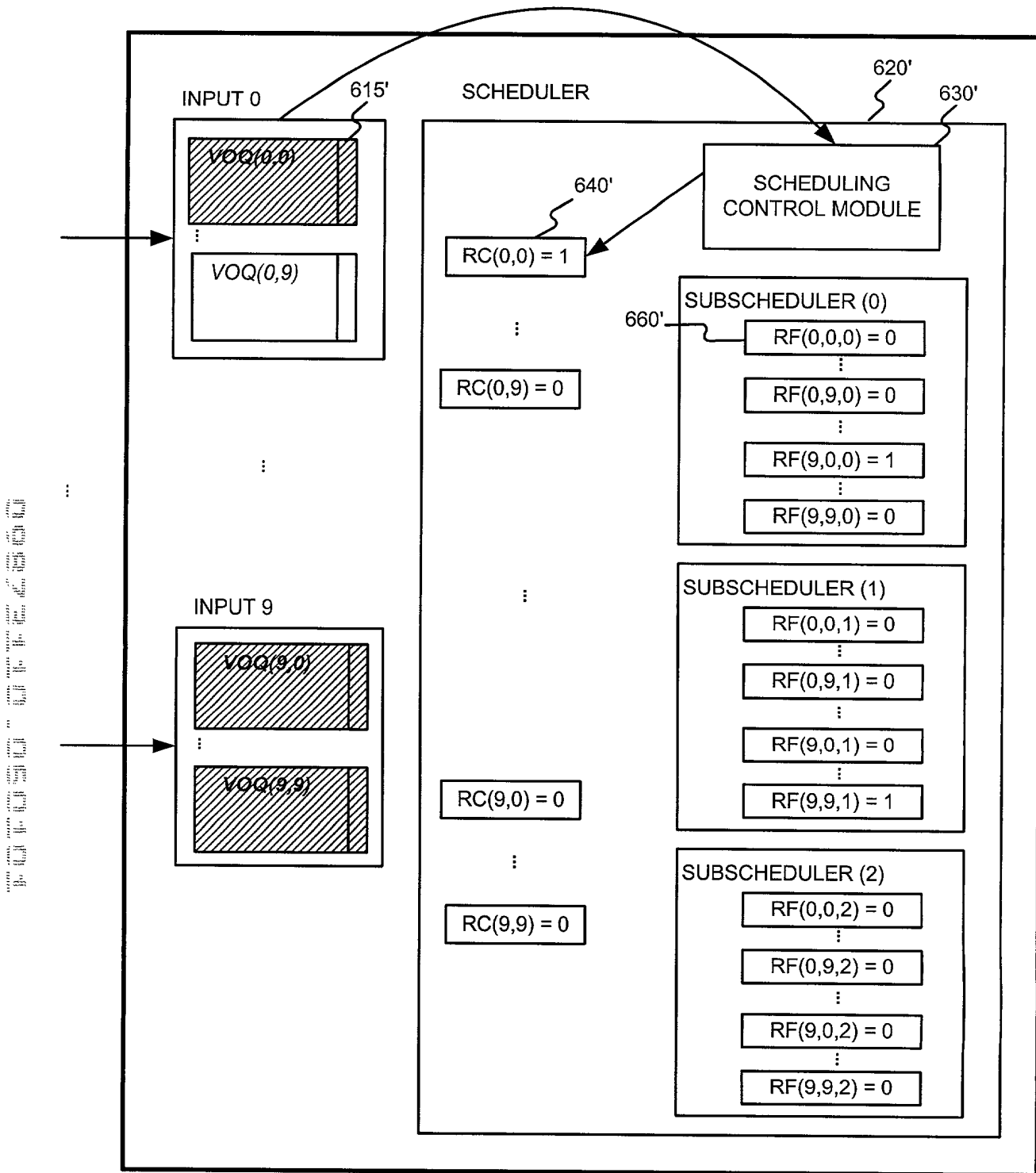


FIGURE 7a

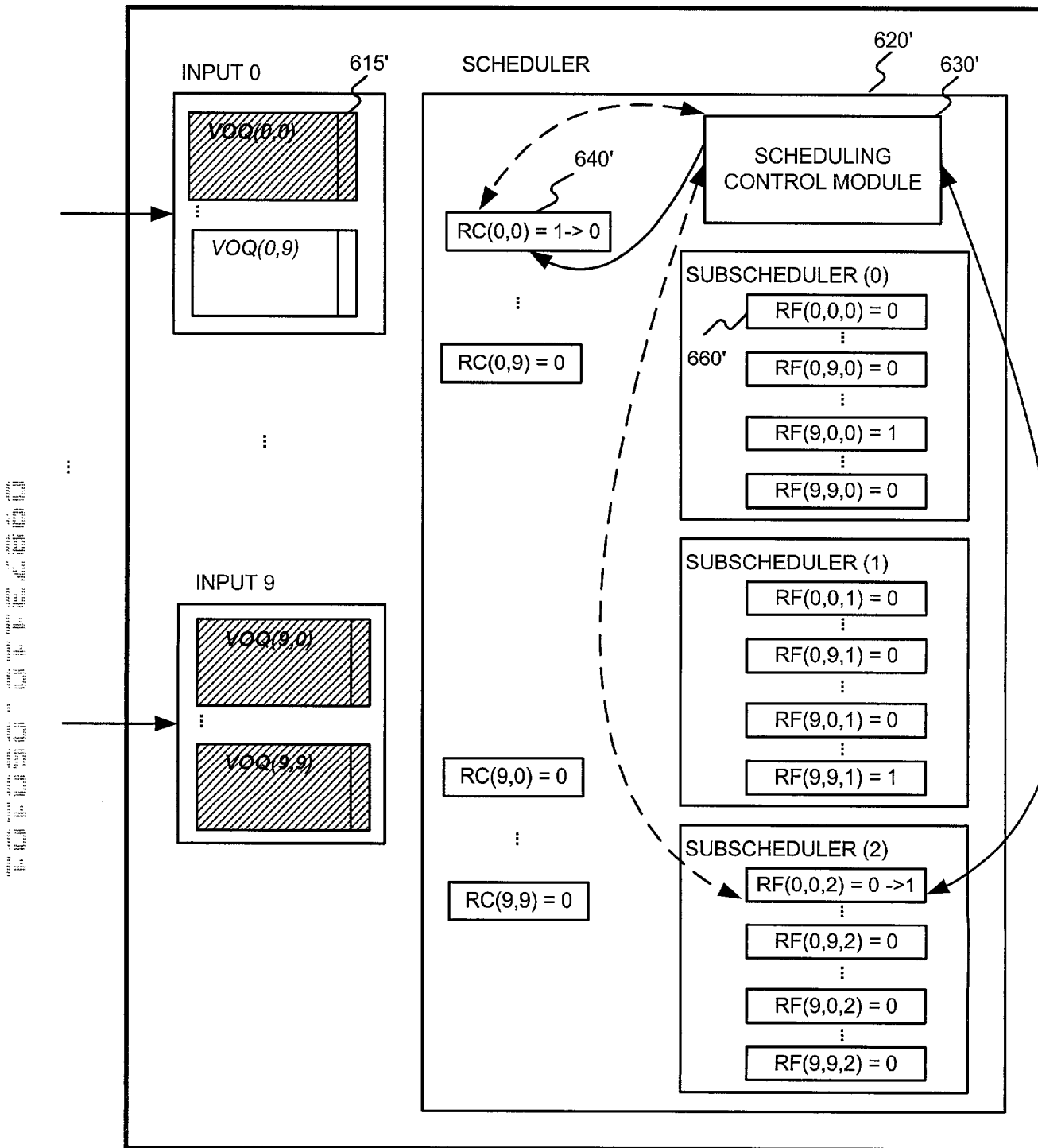


FIGURE 7b

FIG. 7c is a block diagram of a scheduler system. The system includes multiple inputs (INPUT 0 to INPUT 9) and a central SCHEDULER. Each input has a VOQ (Virtual Output Queue) structure. The SCHEDULER contains a SCHEDULING CONTROL MODULE and multiple SUBSCHEDULERS (0 to 2). The SCHEDULING CONTROL MODULE is connected to the VOQs and the SUBSCHEDULERS. The SUBSCHEDULERS contain RF (Round Robin) modules. The SCHEDULING CONTROL MODULE is also connected to a SCHEDULING CONTROL MODULE (620') and a SCHEDULING CONTROL MODULE (630').

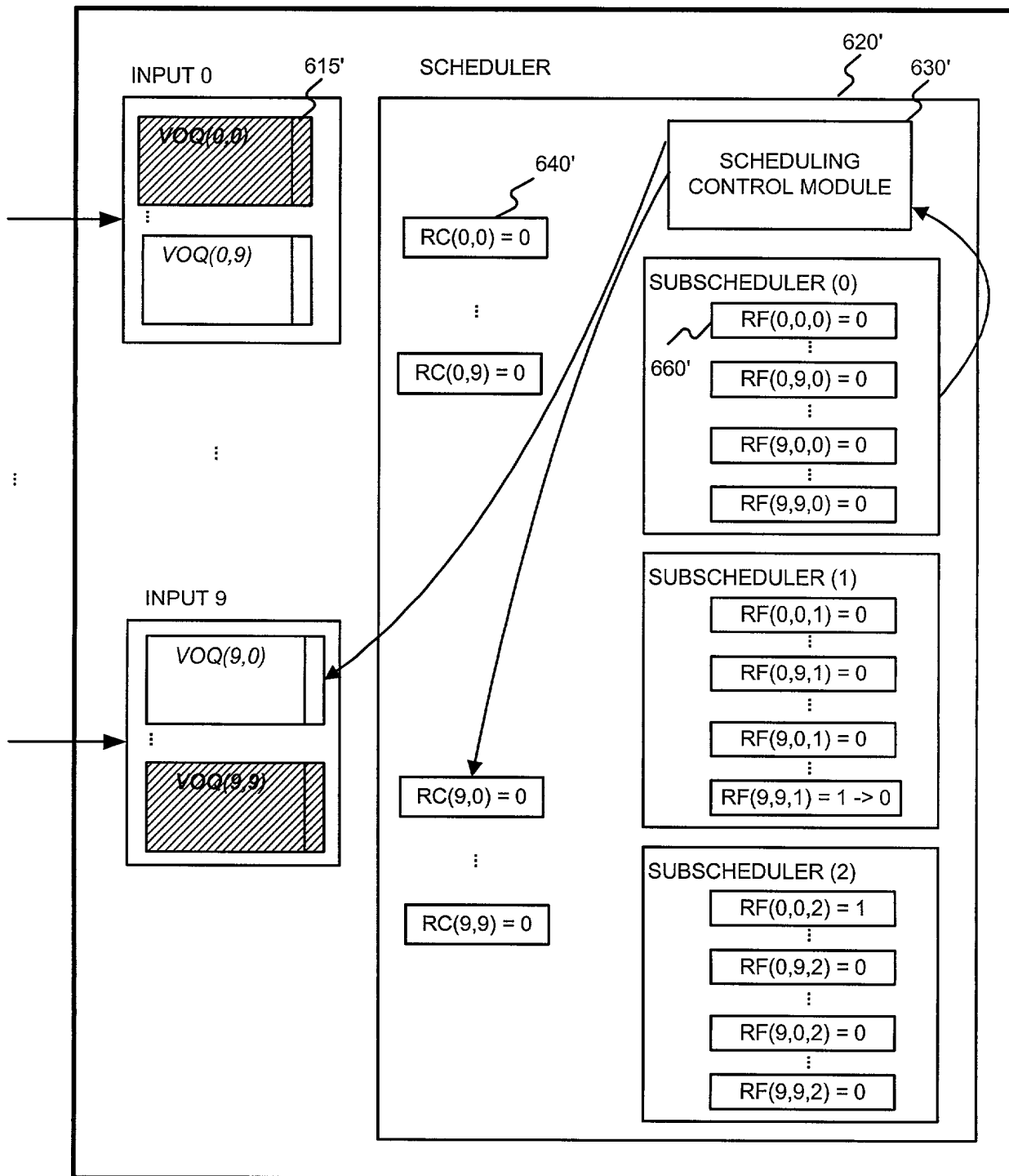


FIGURE 7c

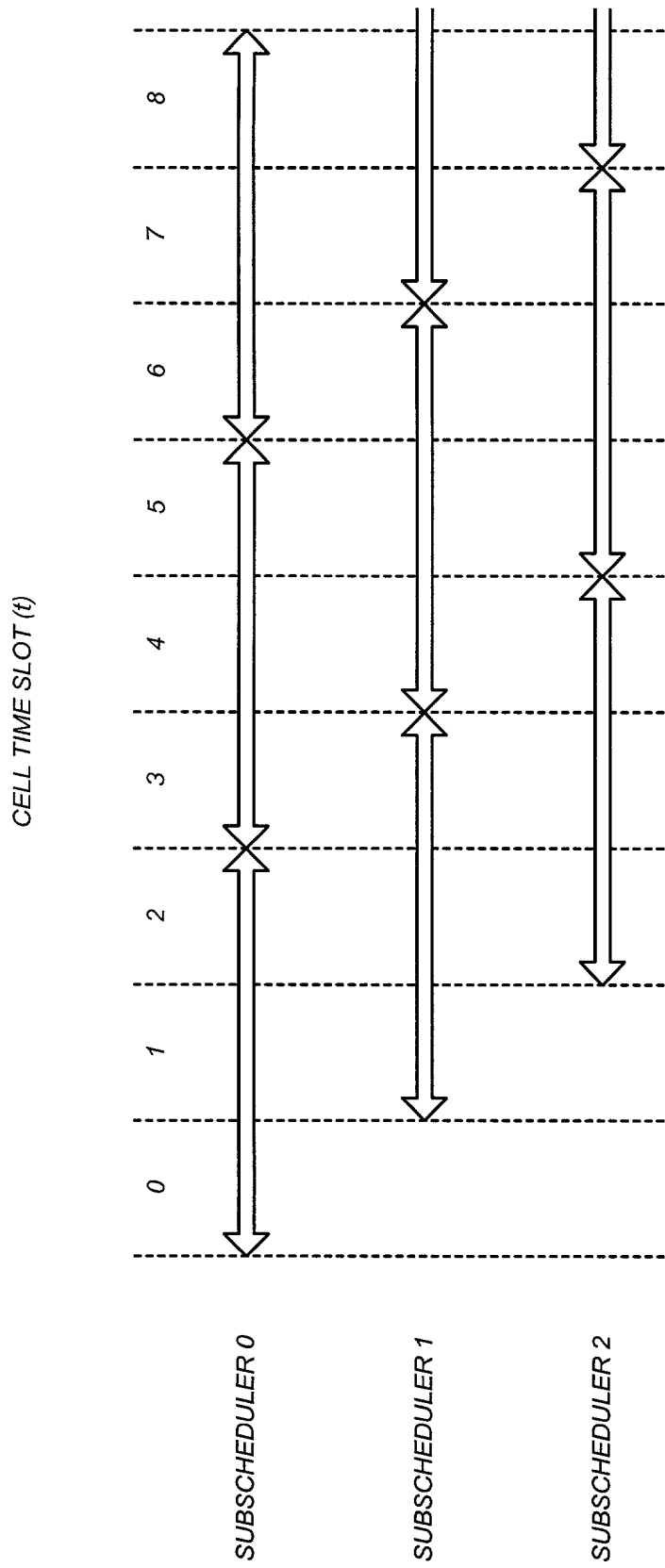


FIGURE 8